

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Serial No:

10/683,838

Docket No:

2011129

Filing Date: 10/09/2003

Applicant:

Hsieh, et al.

Examiner:

TRAIL, ALLYSON NEEL

Art Unit:

2876

Title:

STACKED SMALL MEMORY CARD

To:

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

# TRANSMITTAL OF AMENDMENT

- 1. Transmitted herewith is a response to an office action for this application prepared and submitted by the Applicant.
  - 2. Applicant is entitled to Small Entity Status.
- 3. The proceedings herein are for a patent application and the provisions of 37 CFR 1.136 apply. Applicant believes that no extension of time is required. However, a conditional petition is hereby made to provide for the possibility that applicant has inadvertently overlooked the need for a petition for extension of time.
  - 4. No fee is required at this time.
- 5. Certificate of Mailing (37 CFR 1.8a): I hereby certify that this paper (along with any referred to as being attached or enclosed) is being deposited with the United States Postal Service on the date shown below with sufficient postage as First Class Mail in an envelope addressed to: Mail Stop Non-Fee Amendment, Commissioner for Patents, P.O. Box 1450, Arlington, VA 22313-1450.

Respectfully submitted,

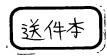
Date: December 16, 2004

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To:

THE ASSISTANT COMMISSIONER FOR PATENTS

Washington, D.C. 20231

## RESPONSE TO OFFICE ACTION

Dear Sir:

In response to the Office Action dated 10/19/2003, Applicant submits the following amendment.

#### **AMENDMENT**

#### IN THE SPECIFICATION

## STACKED SMALL MEMORY CARD

#### BACKGROUND OF THE INVENTION

10 Field of the invention

> The present invention relates to a stacked small memory card, and in particular to a stacked small memory card that has a long lifetime and high durability.

## Description of the Related Art

15 The prior method for producing a general memory card always packs chips to single integrated circuit, then mounts the IC onto a printed circuit board by the way of surface mount technique (SMT). The chip may be a memory element, such as flash memory. The golden fingers mounted on the printed circuit board for